





SHEET 1 OF 1

INFORMATION DISCLOSURE  
CITATION IN AN  
APPLICATION

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**2825**

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code <sub>2</sub> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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## FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number 4-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
		WO 01/90882 A2	11/29/2001	RADIOSCOPE LIMITED		X

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
NL		KIM, Tae-Woo., et al. "Hardware Cost Estimation Techniques for C-Level Description." VLSI and CAD, 1999, ICVC, 6th International Conference of Seoul, South Korea, October 26-27, IEEE US, XP010370108, pp. 85-88
NL		CARRO, Luigi., et al. "System Synthesis for Multiprocessor Embedded Applications." Design, Automation and Test in Europe, March 27, 2000, XP010377540, pp. 697-702
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EXAMINER

Nelson L

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3/24/06

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